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normal cycle is maintained, while when SC is received, thus becoming wait-state (STEP S205).

(6) FIG. 6 is signal timing chart showing rapid-auto-scan when signal is incapable of being received. When rapid-scan is implemented by switch manipulation, cycle interval of frequency scan is shortened. The number of rapid-scan is counted by the CPU. When synchronous section can not be received after repeating the scan within determined number of times, the rapid-scan is stopped. The reason why it causes the rapid scan to stop within the determined number of times, because consumption amount of battery is increased due to long receiving operation.

(7) When there is manipulation of the switch 108, it becomes waiting of selective-input whether or not rapid-scan is implemented (STEP S206). At this time, when the rapid-scan is not selected by manipulation of the switch 109, auto-scan of the normal cycle interval is maintained. While when the rapid scan is selected, it permits the rapid scan to implement (STEP S207). When the ra